N Channel Enhancement Mode Power MOSFET

GENERAL DESCRIPTION

The product utilizes the advanced planar processing techniques to achieve the high cell density and reduces the on-resistance with high repetitive avalanche rating. These features combine to make this design an extremely efficient and reliable device for use in power switching application and a wide variety of other applications.

FEATURES

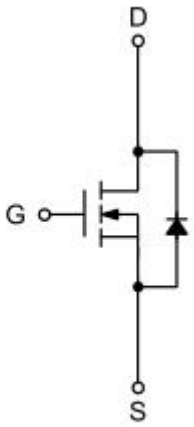
- 500V/8A, $R_{DS(ON)}$ =0.75Ω@V_{GS}=10V(Typical)
- Fast switching and reverse body recovery
- Excellent package for good heat dissipation

APPLICATIONS

- Lighting
- High efficiency switch mode power supplies

PIN DESCRIPTION

TO-252 -2Ltop view



Absolute Maximum Ratings(Tc=25° C Unless Otherwise Noted)

Symbol	Parameter		Rating	Unit
V_{DS}	Drain-Source Voltage		500	V
V_{GS}	Gate-Source Voltage		±30	V
I_D	Continuous Drain Current	Tc=25° C	8	A
		Tc=100° C	4.8	
I_{DM}	Pulsed Drain Current		30	A
P_D	Maximum Power Dissipation		80	W
T_J T_{STG}	Operating Junction and Storage Temperature Range		-55 to +150	° C
$R_{\theta JC}$	Thermal Resistance-Junction to Case		1.56	° C/W

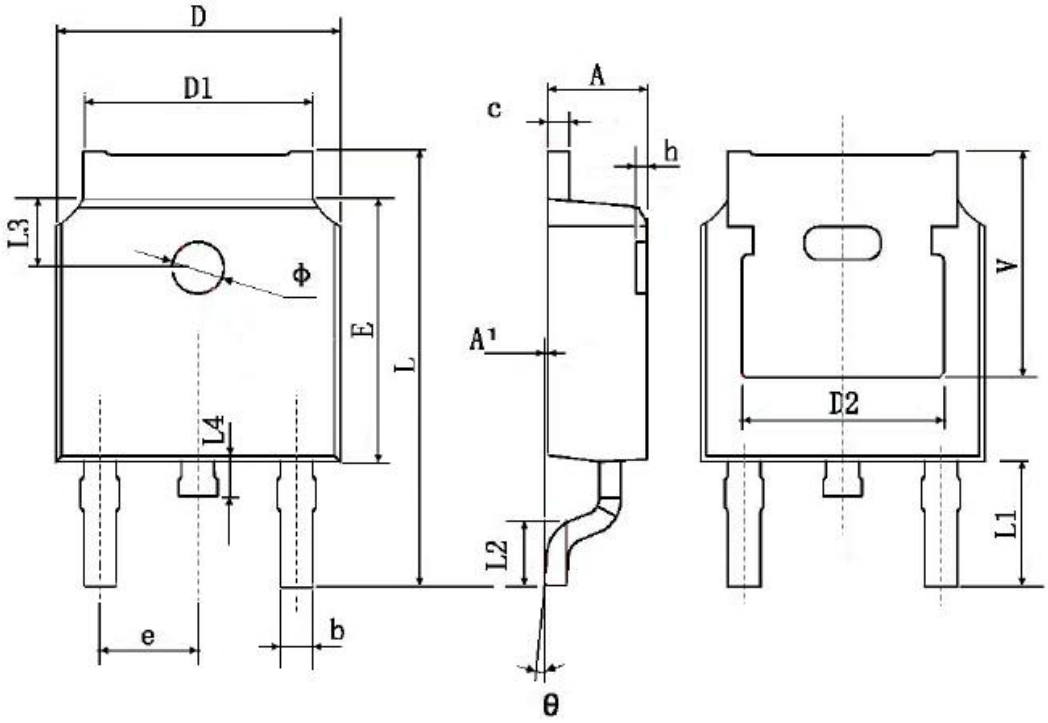
Electrical Characteristics(Tc=25° C Unless Otherwise Noted)

Symbol	Parameter	Conditions	Min	Typ	Max	Unit
Static Characteristics						
BV_{DSS}	Drain-Source Breakdown Voltage	V_{GS}=0V,I_{DS}=250uA	500			V
I_{DSS}	Zero Gate Voltage Drain Current	V_{DS}=500V,V_{GS}=0V			1	uA
I_{GSS}	Gate-Body Leakage Current	V_{GS}=±30V,V_{DS}=0V			±100	nA
$V_{GS(th)}$	Gate Threshold Voltage	V_{DS}= V_{GS}, I_{DS}=250uA	2.0		4.0	V
$R_{DS(ON)}$	Drain-Source On-state Resistance	V_{GS}=10V,I_{DS}=4A		0.75	0.85	Ω

Electrical Characteristics(Tc=25ºC Unless Otherwise Noted)

Symbol	Parameter	Conditions	Min	Typ	Max	Unit
Drain-Source Diode Characteristics						
V_{SD}	Diode Forward Voltage	V_{GS}=0V,I_{SD}=4A			1.4	V
Trr	Reverse Recovery Time	I_{SD}=8A		390		ns
Qrr	Reverse Recovery Charge	di/dt=100A/us		2.6		nC
Dynamic Characteristics						
$T_{d(on)}$	Turn-on Delay Time	V_{DS}=250V, R_G=25Ω, I_{DS} =8A, V_{GS}=10V,		16		ns
Tr	Turn-on Rise Time			12		
$T_{d(off)}$	Turn-off Delay Time			58		
T_f	Turn-off Fall Time			26		
C_{ISS}	Input Capacitance	V_{GS}=0V, V_{DS}=25V, f=1.0MHz		795		pF
C_{OSS}	Output Capacitance			68		
C_{RSS}	Reverse Transfer Capacitance			8.1		
Q_g	Total Gate Charge	V_{DS}=400V,I_D=8A, V_{GS}=10V		15.5		nC
Q_{gs}	Gate-Source Charge			4.6		
Q_{gd}	Gate-Drain Charge			4.1		

TO-252 Package Outline



Symbol	Dimensions In Millimeters		Dimensions In Inches	
	Min.	Max.	Min.	Max.
A	2.200	2.400	0.087	0.094
A1	0.000	0.127	0.000	0.005
b	0.660	0.860	0.026	0.034
c	0.460	0.580	0.018	0.023
D	6.500	6.700	0.256	0.264
D1	5.100	5.460	0.201	0.215
D2	0.483 TYP.		0.190 TYP.	
E	6.000	6.200	0.236	0.244
e	2.186	2.386	0.086	0.094
L	9.800	10.400	0.386	0.409
L1	2.900 TYP.		0.114 TYP.	
L2	1.400	1.700	0.055	0.067
L3	1.600 TYP.		0.063 TYP.	
L4	0.600	1.000	0.024	0.039
Φ	1.100	1.300	0.043	0.051
θ	0°	8°	0°	8°
h	0.000	0.300	0.000	0.012
V	5.350 TYP.		0.211 TYP.	